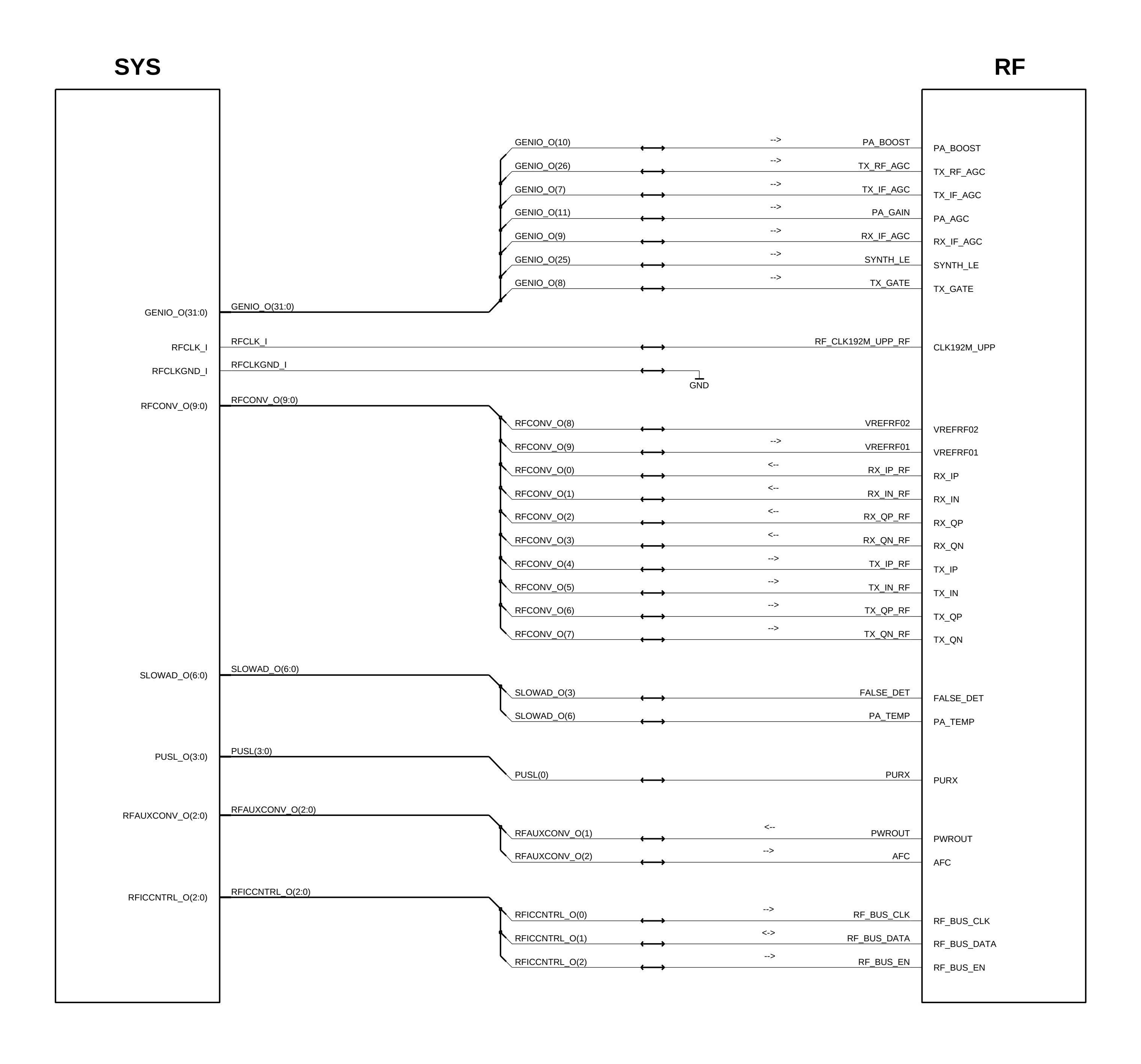
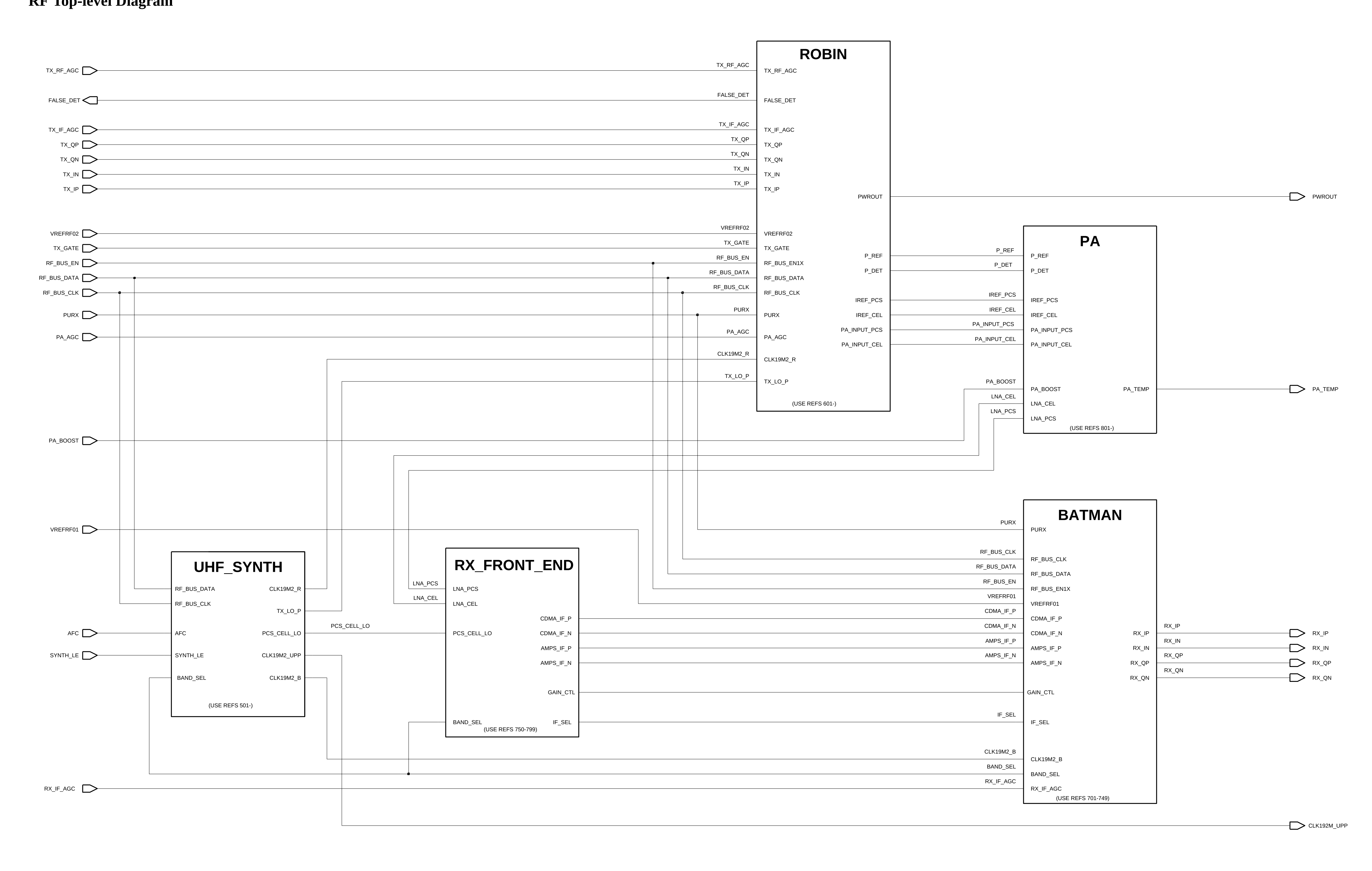
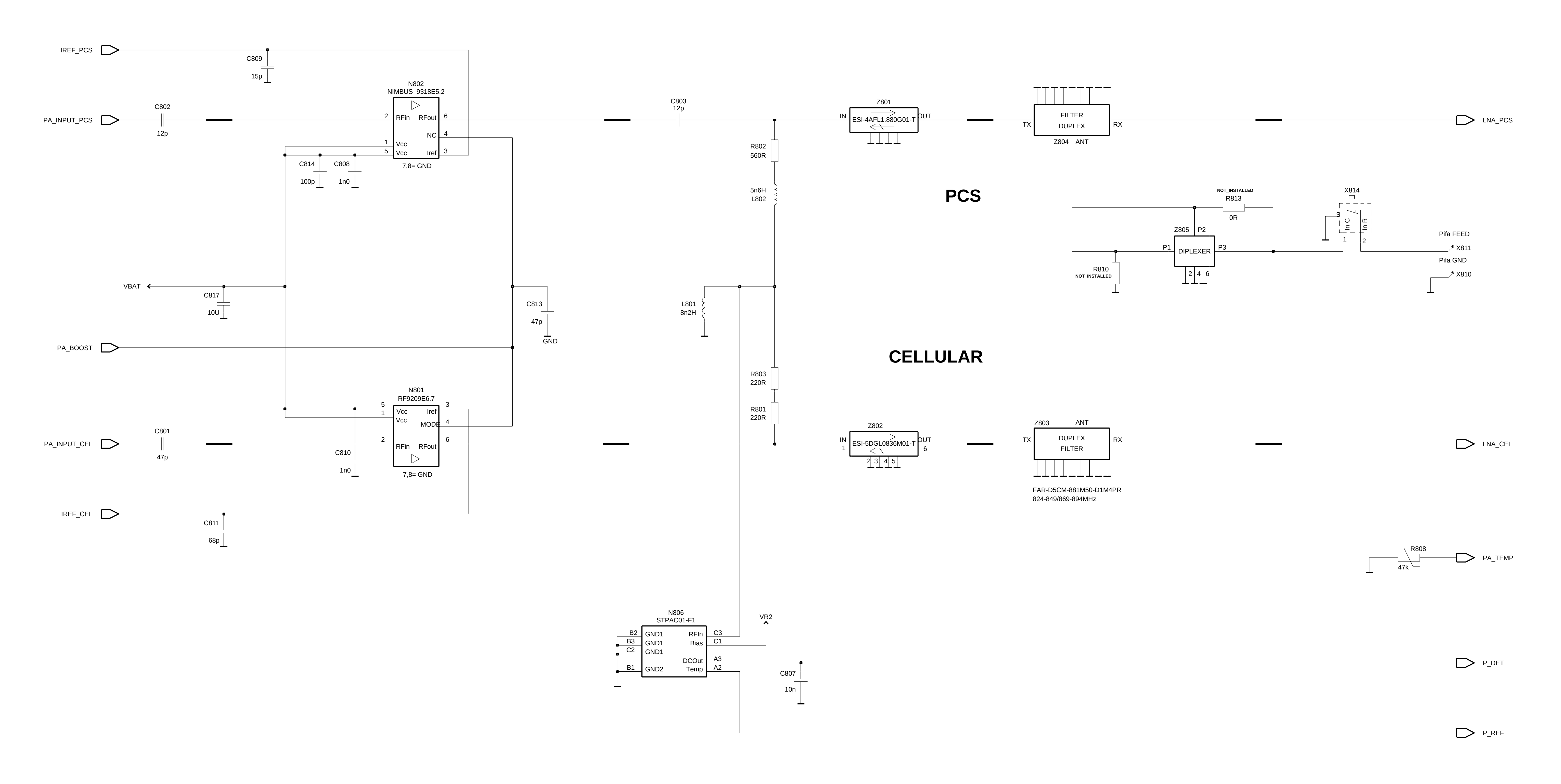
Top-level Diagram



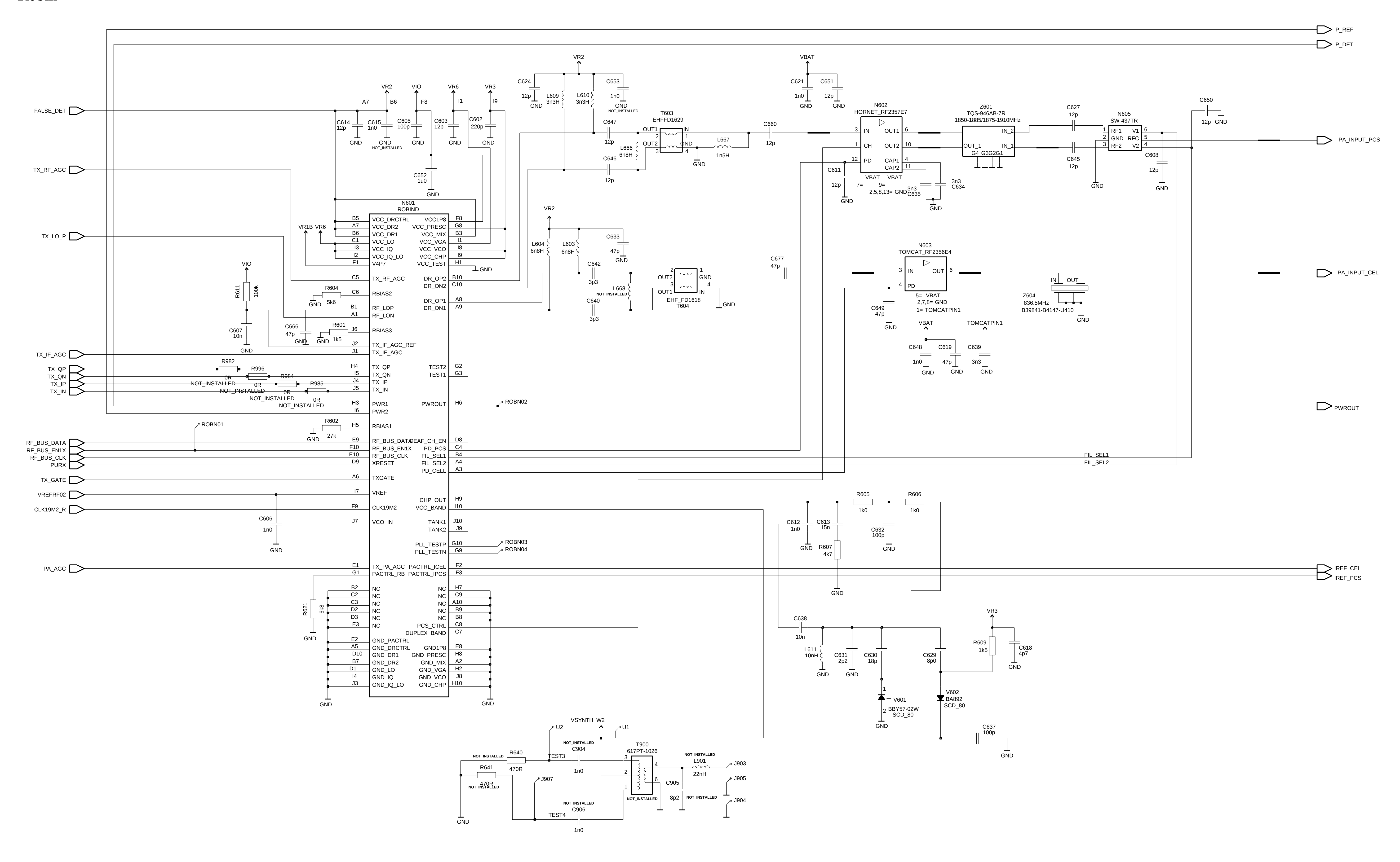
RF Top-level Diagram



PA

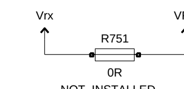

IREF_PCS
C809
15p
N802
NIMBUS_9318E5.2
PA_INPUT_PCS
C802
12p
RFin
RFout
NC
Vcc
Vcc
Iref
7,8= GND
C814
100p
C808
1n0
C803
12p
Z801
ESI-4AFL1.880G01-T
IN
OUT
FILTER
DUPLEX
TX
RX
Z804
ANT
LNA_PCS
R802
560R
5n6H
L802
PCS
NOT_INSTALLED
R813
0R
X814
Z805
P2
P1
DIPLEXER
P3
R810
NOT_INSTALLED
In C
In R
Pifa FEED
X811
Pifa GND
X810
VBAT
C817
10U
C813
47p
GND
L801
8n2H
PA_BOOST
CELLULAR
R803
220R
R801
220R
N801
RF9209E6.7
Vcc
Vcc
Iref
MODE
RFin
RFout
PA_INPUT_CEL
C801
47p
C810
1n0
7,8= GND
Z802
ESI-5DGL0836M01-T
Z803
ANT
DUPLEX
FILTER
LNA_CEL
FAR-D5CM-881M50-D1M4PR
824-849/869-894MHz
IREF_CEL
C811
68p
R808
47k
PA_TEMP
N806
STPAC01-F1
GND1
GND1
GND1
GND2
RFIn
Bias
DCOut
Temp
VR2
C807
10n
P_DET
P_REF

Robin



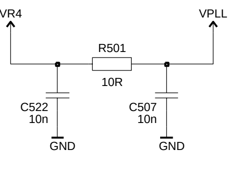
P_REF
P_DET
FALSE_DET
TX_RF_AGC
TX_LO_P
TX_IF_AGC
TX_QP
TX_QN
TX_IP
TX_IN
RF_BUS_DATA
RF_BUS_EN1X
RF_BUS_CLK
PURX
TX_GATE
VREFRF02
CLK19M2_R
PA_AGC
N601
ROBIND
N602
HORNET_RF2357E7
N603
TOMCAT_RF2356E4
Z601
TQS-946AB-7R
1850-1885/1875-1910MHz
N605
SW-437TR
Z604
836.5MHz
B39841-B4147-U410
T603
EHFFD1629
T604
EHF_FD1618
T900
617PT-1026
V601
BBY57-02W
SCD_80
V602
BA892
SCD_80
PA_INPUT_PCS
PA_INPUT_CEL
PWROUT
IREF_CEL
IREF_PCS
FIL_SEL1
FIL_SEL2
VSYNTH_W2
NOT_INSTALLED
GND

Front End

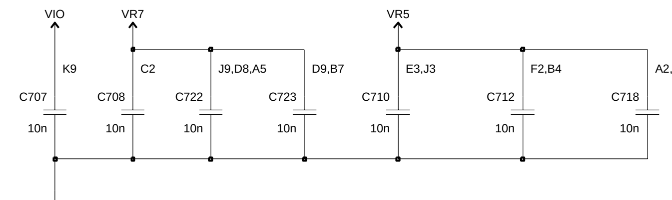


	LOGIC INPUTS		
MODES	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

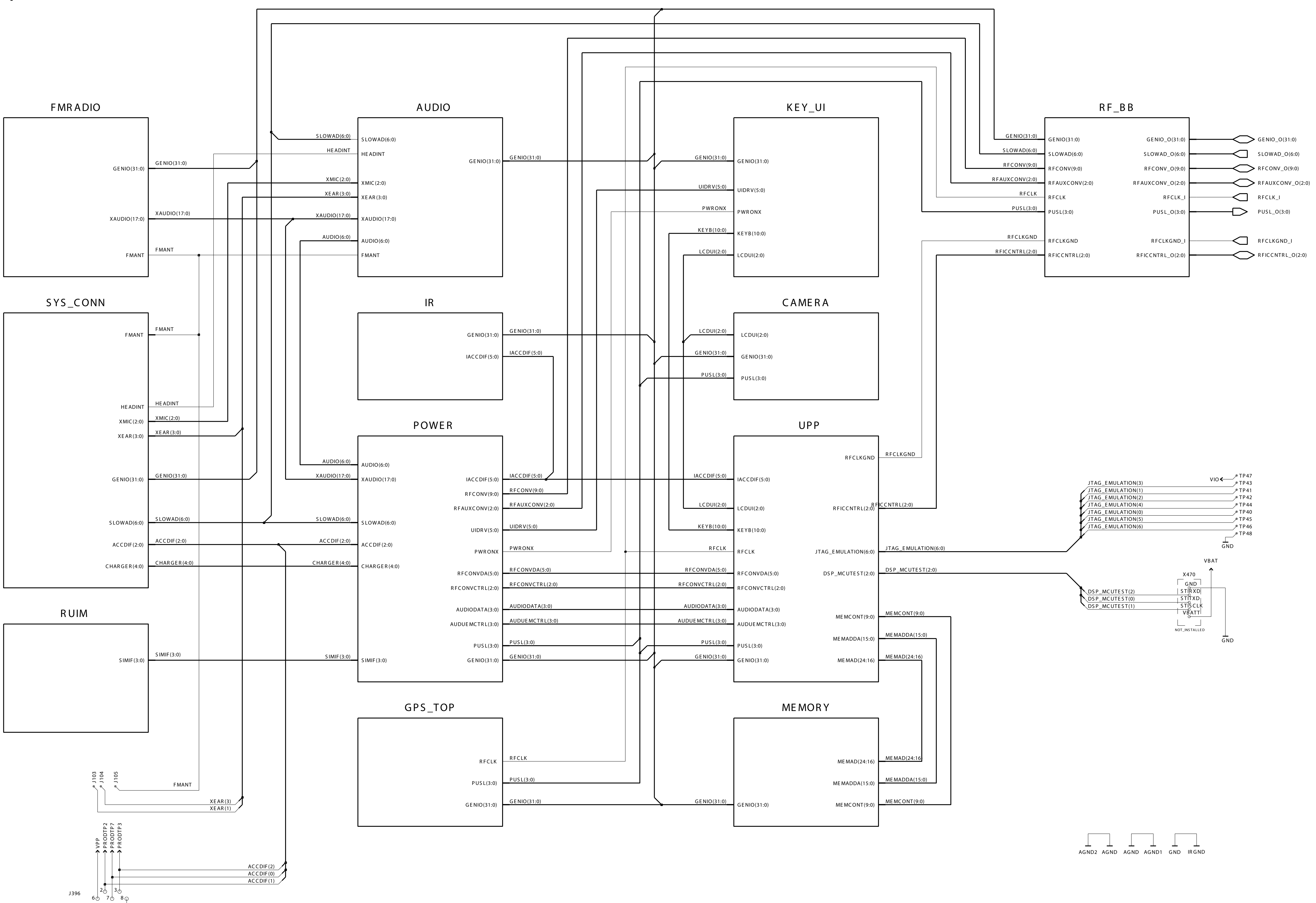
UHF Synthesizer



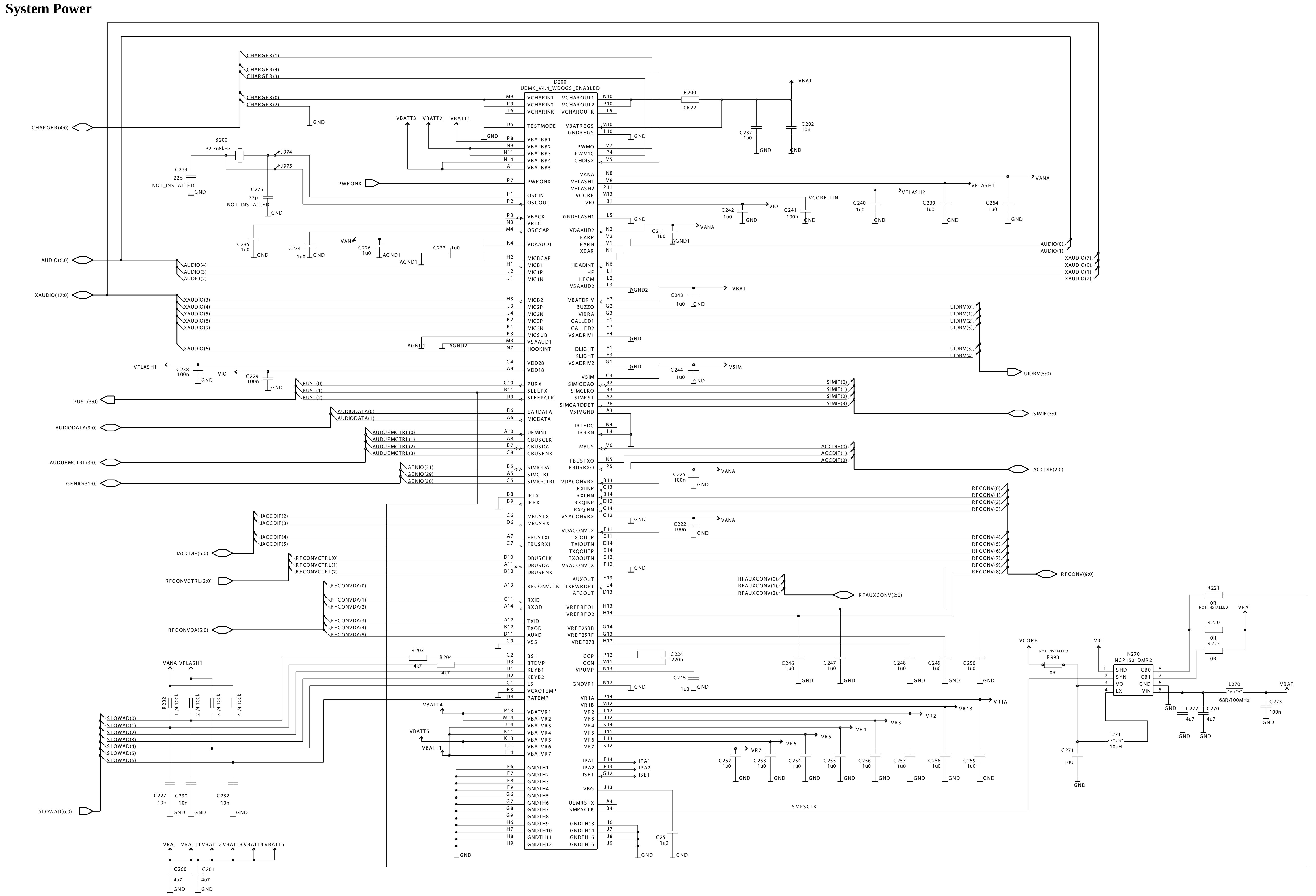
Batman


VIO
VR7
VR5
K9
C2
J9,D8,A5
D9,B7
E3,J3
F2,B4
A2,C3
C707
C708
C722
C723
C710
C712
C718
10n

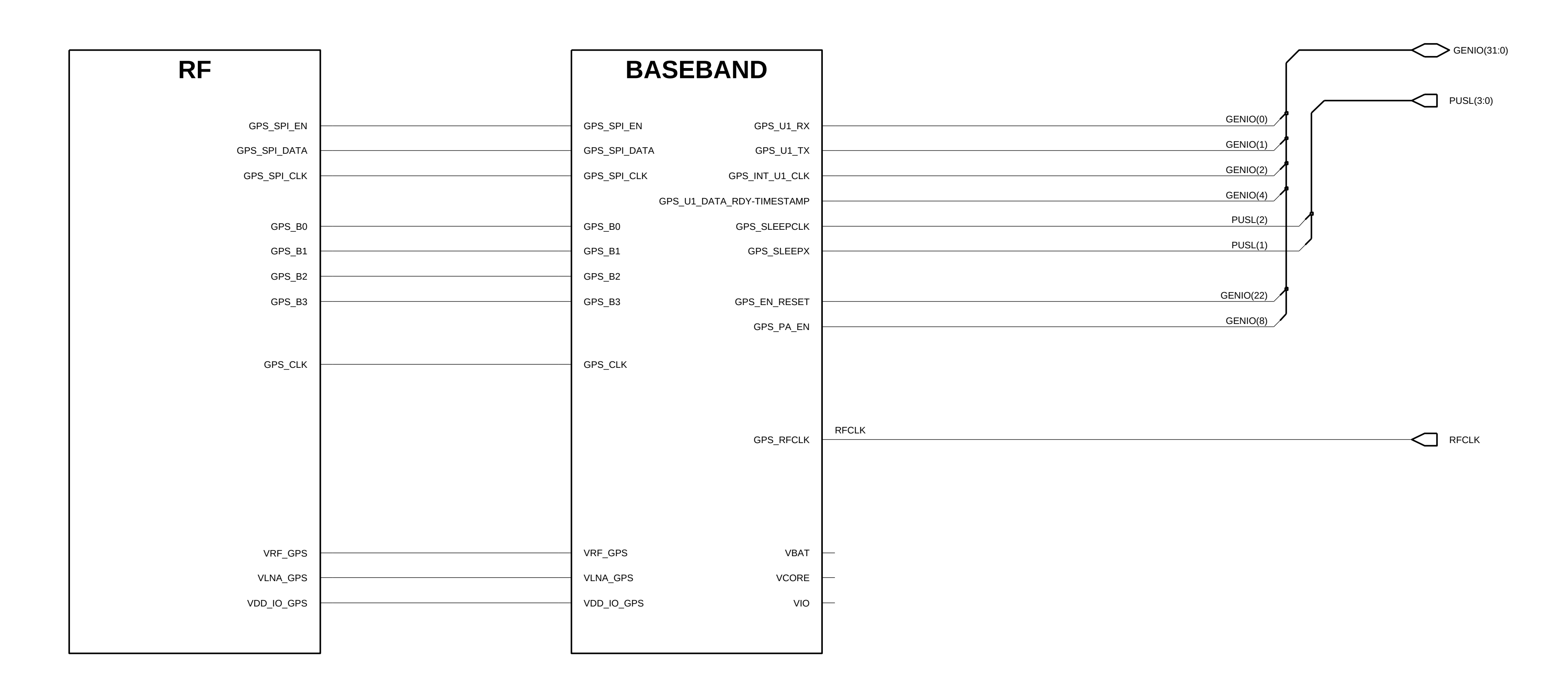
System Module



System Power

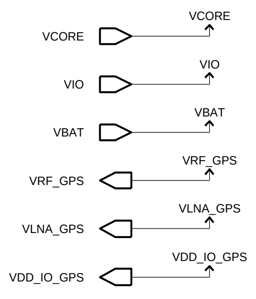


GPS RF-BB Interface

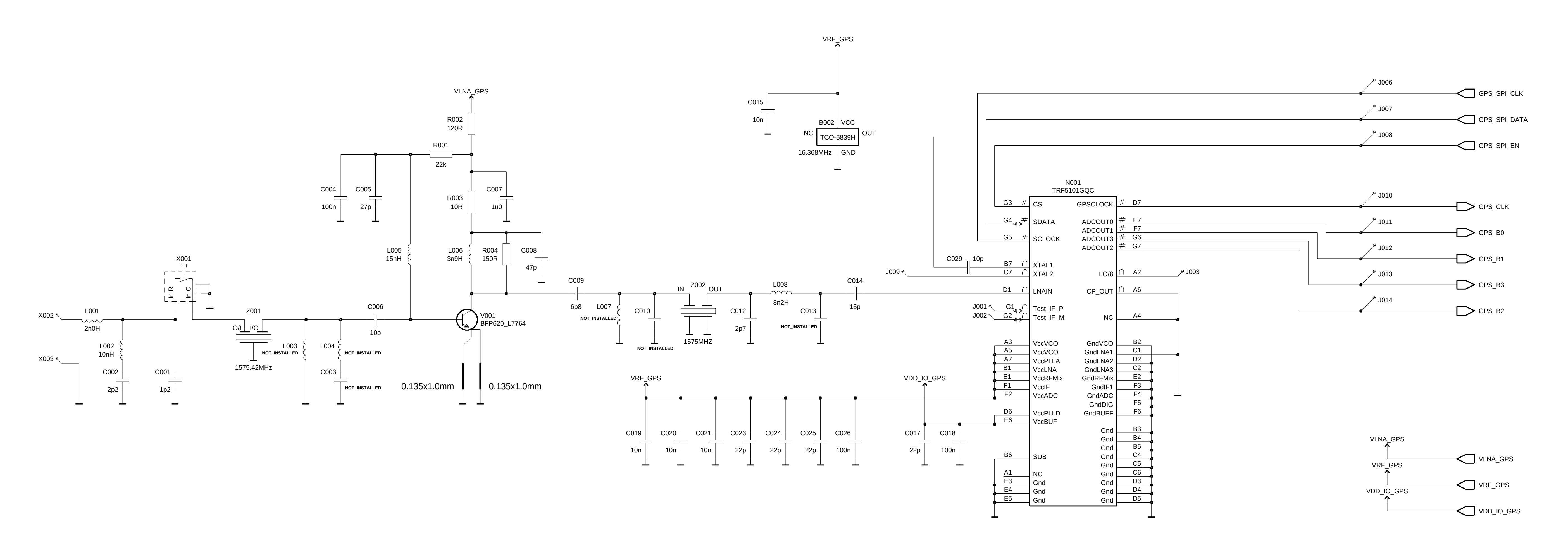


GPS Baseband Circuit Diagram

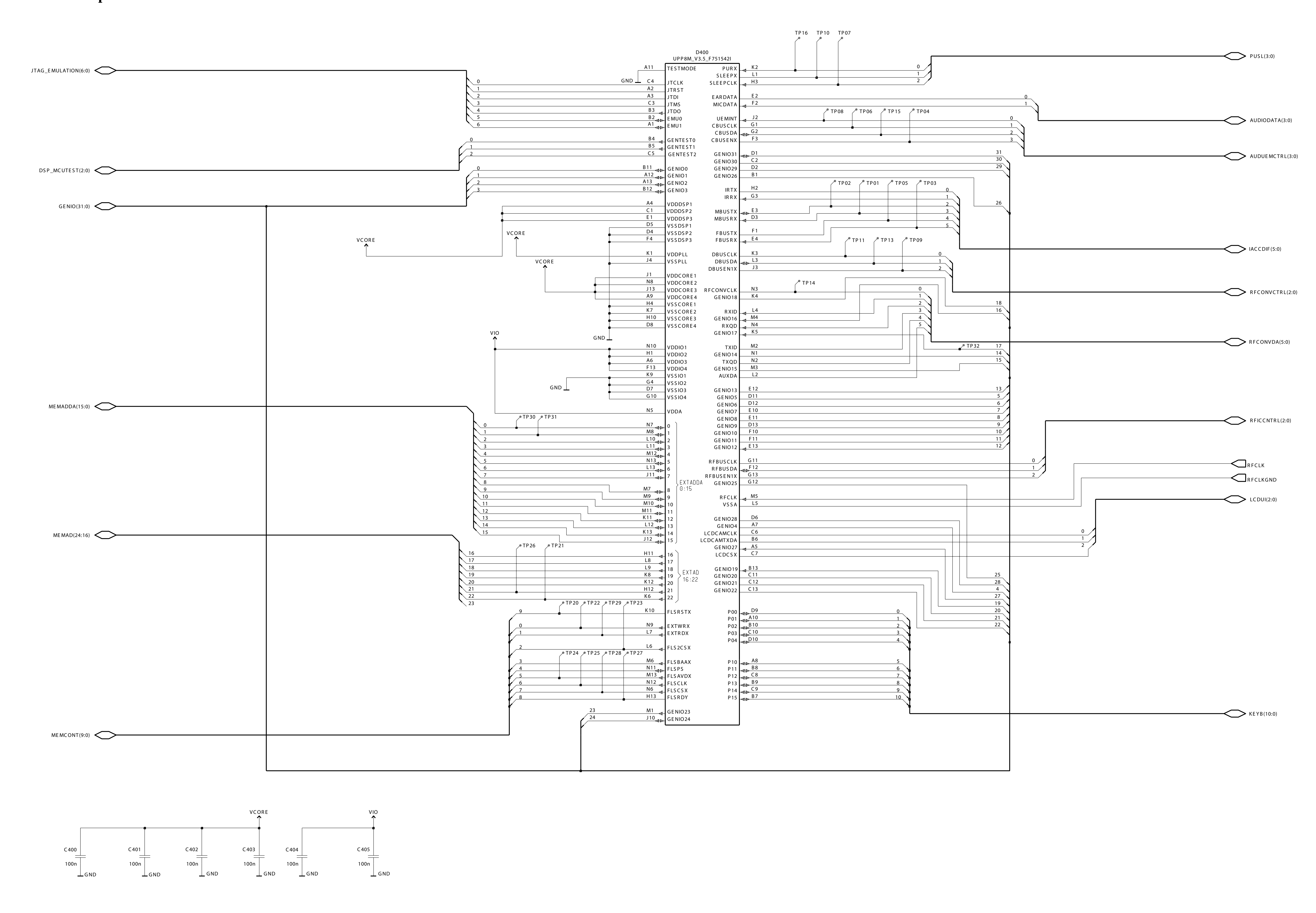
VCORE
VDD_CORE_GPS
R057
0R
VIO
VDD_IO_GPS
R053
0R
R073 0R
R074 0R
GND VSS PINS=
DATA15 DATA14 DATA13 DATA12 DATA11 DATA10 DATA9 DATA8 DATA7 DATA6 DATA5 DATA4 DATA3 DATA2 DATA1 DATA0
ADDR21 ADDR20 ADDR19 ADDR18 ADDR17 ADDR16 ADDR15 ADDR14 ADDR13 ADDR12 ADDR11 ADDR10 ADDR9 ADDR8 ADDR7 ADDR6 ADDR5 ADDR4 ADDR3 ADDR2 ADDR1
EXINTR_N
PG_PULSE_N
FRAMESTART
TIMESTAMP/IO9
PA_EN/IO8
SPARE3/IO7
SPARE2/IO6
SPARE1/IO5
RF_LNA_EN/IO3
SPI_EN/IO2
SPI_DATA/IO§1
SPI_CLK/IO0
RF_REG_EN
OEN
WEN
FLASH_CEN
RAM_LBN
RAM_UBN
RAM_CEN
RSVD_CEN
VSSa
VDDa
VDDram VDDram VDDram
VDDcore VDDcore VDDcore VDDcore
VDDio VDDio VDDio VDDio VDDio
SIGNAL_I3 SIGNAL_I2 SIGNAL_I1 SIGNAL_I0
INT_BOOT_N
AI2_IS801_N
I2C_SDA
I2C_SCL
U2TX
U2RX
INT_U1CLK/IO4
U1DATA_RDY
U1TX
U1RX
SLAVE_MODE
CHIPSEL_IN_N
TEST_MODE
EXTERN0
NIRQ
DBGEN
TDO TDI TMS TCK NTRST
SLEEPX_N
GPS_EN_RESET
REF_CLK
BYPASS
RTC_CLK
GPS_CLK
TWL5001_V1.2
D051
VDD_IO_GPS
C051 C052 C054
100n 100n 100n
VDD_CORE_GPS
C056 C057 C058 C060
100n 100n 100n 100n
GPS_I2C_DATA J051
GPS_I2C_CLOCK J052
VDD_IO_GPS
R052
100k
GPS_U2TX J066
GPS_U2RX J067
J068
GPS_INT_U1_CLK J055
GPS_U1_TX J057
GPS_U1_RX J058
GPS_U1_DATA_RDY-TIMESTAMP J056
GPS_EN_RESET J061
GPS_RFCLK J062
GPS_PA_EN J064
VDD_IO_GPS
R059
NOT_INSTALLED
R054
0R
R060
0R
C068 10n
GPS_B3
GPS_B2
GPS_B1
GPS_B0
GPS_PA_EN
GPS_SLEEPCLK
GPS_RFCLK
GPS_EN_RESET
GPS_SLEEPX
GPS_U1_DATA_RDY-TIMESTAMP
GPS_U1_RX
GPS_U1_TX
GPS_INT_U1_CLK
GPS_CLK
GPS_SPI_CLK
GPS_SPI_DATA
GPS_SPI_EN
VLNA_GPS
VRF_GPS
VBAT_GPS
VBAT
R051
0R
NOT_INSTALLED
VIN N052
VOUT
LP3985ITLX
-2.8
VEN
BYPASS
GND
C064
2u2
C065
10n
C063
2u2



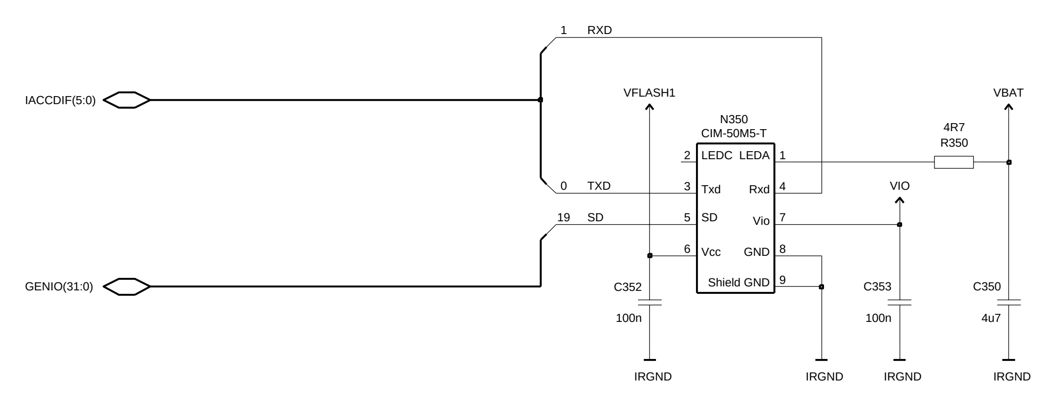
GPS RF Circuit Diagram



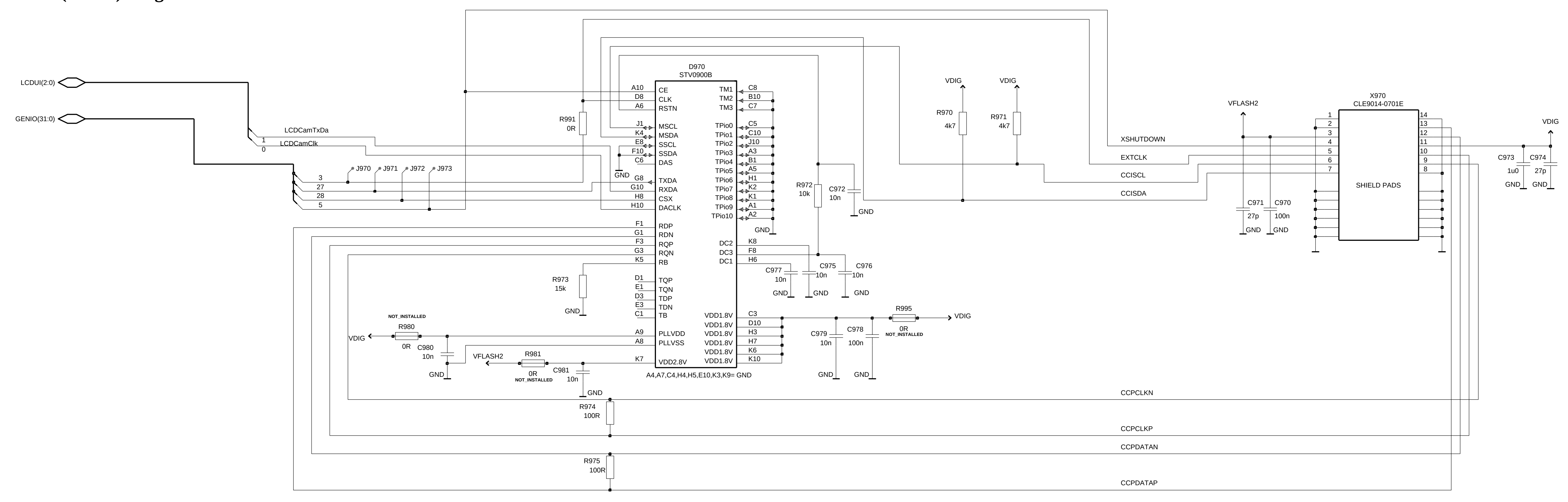
UPP 8M Implementation

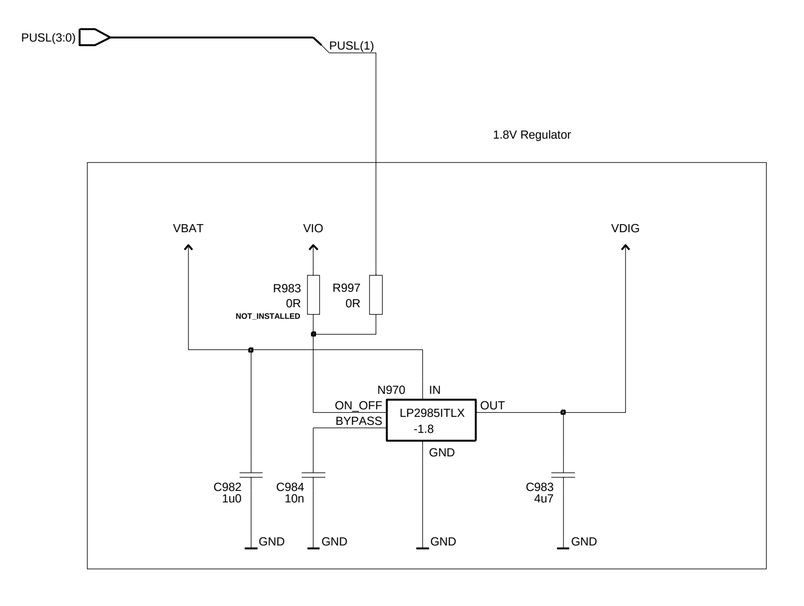


IR Module 1.8V

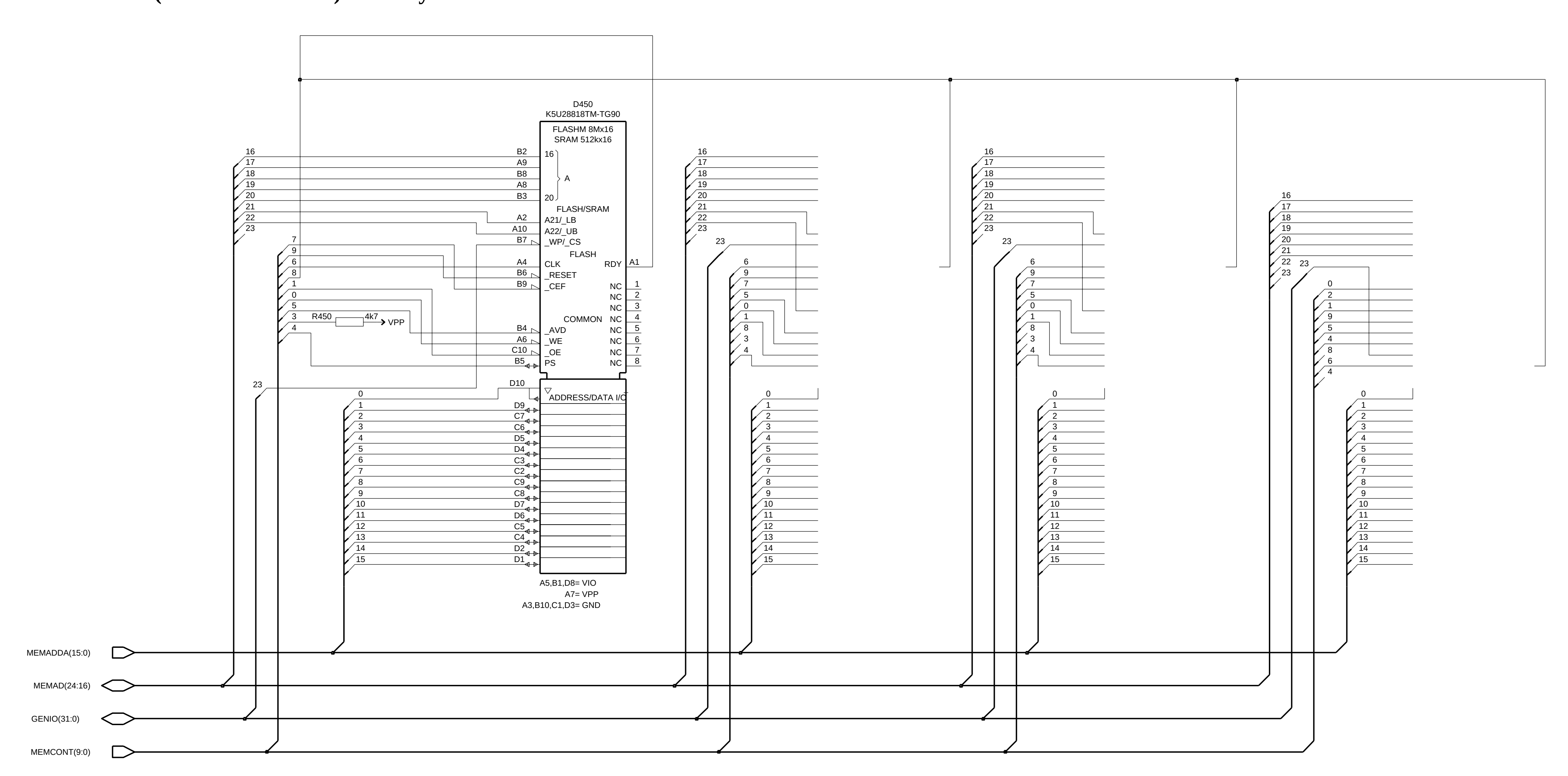


Camera (James) Diagram

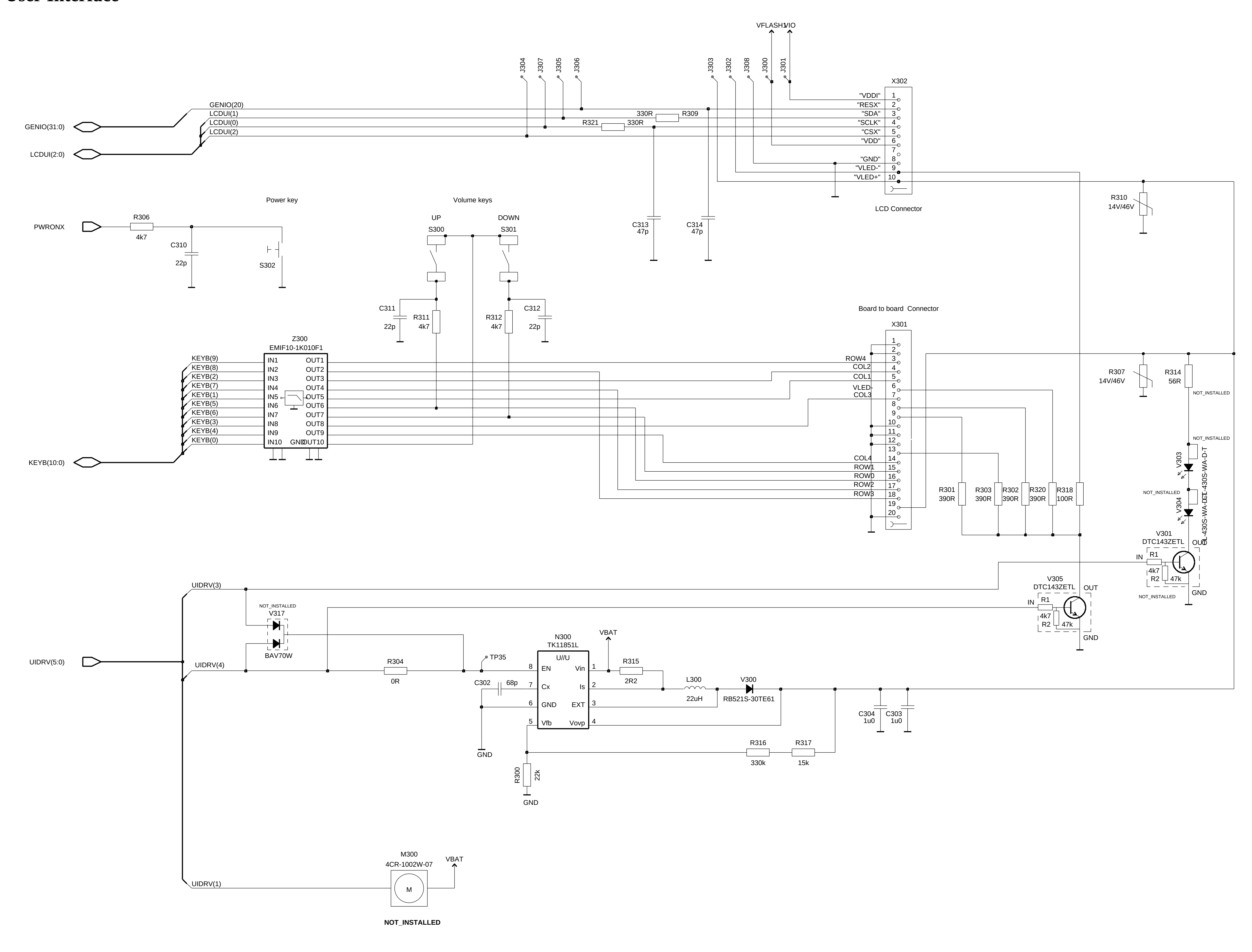




AMD 64-Mbit (Burst Read/Write) Memory



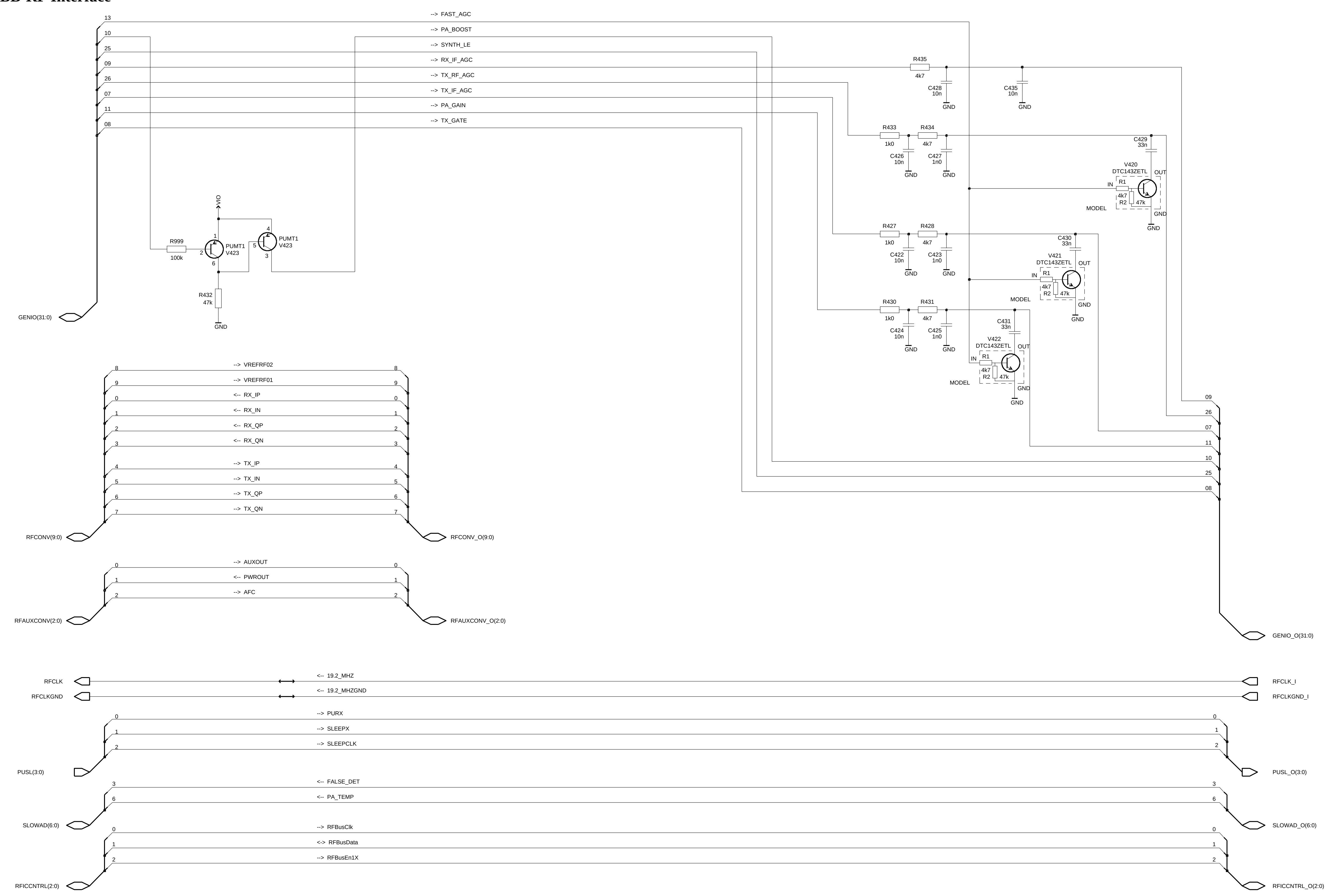
User Interface



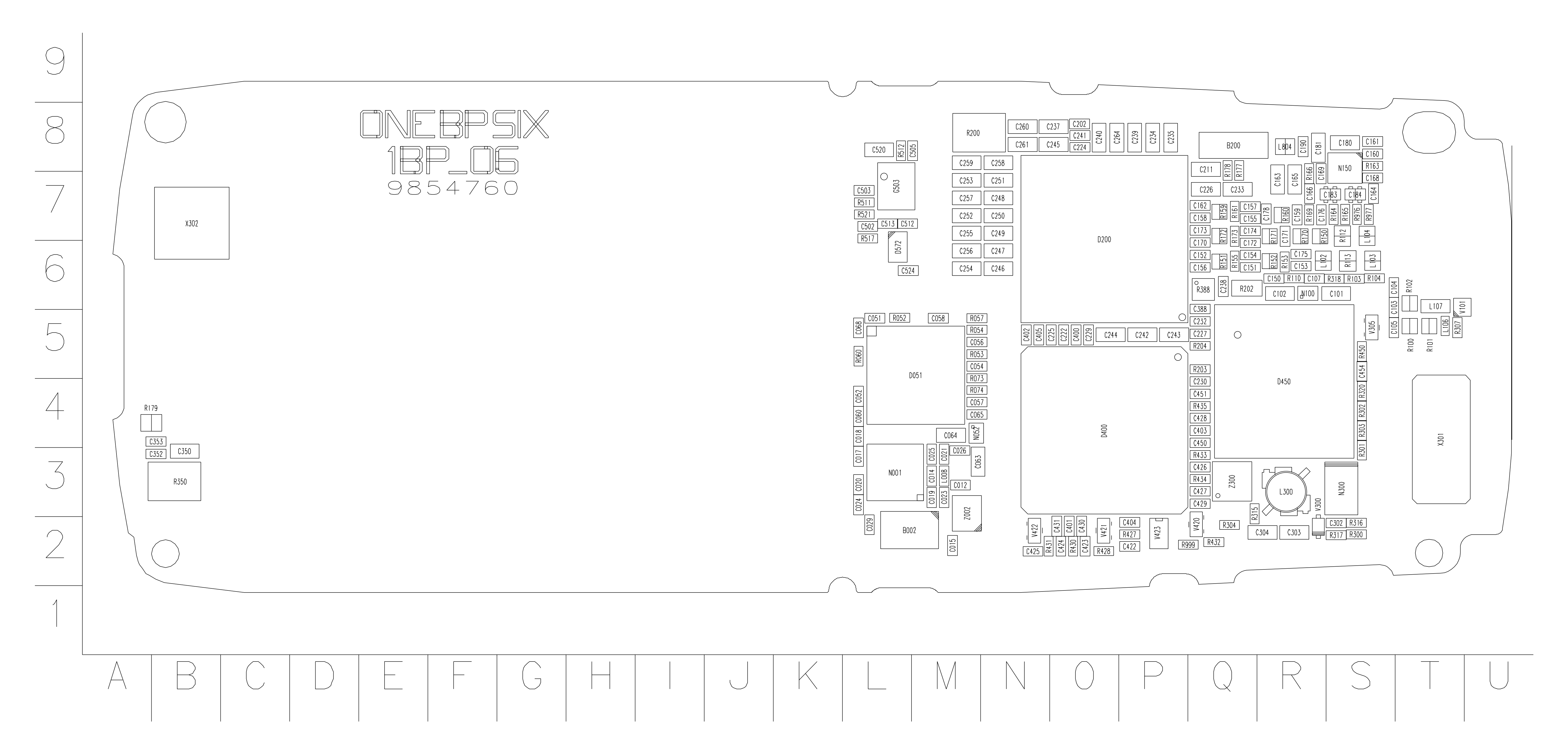
System Connector



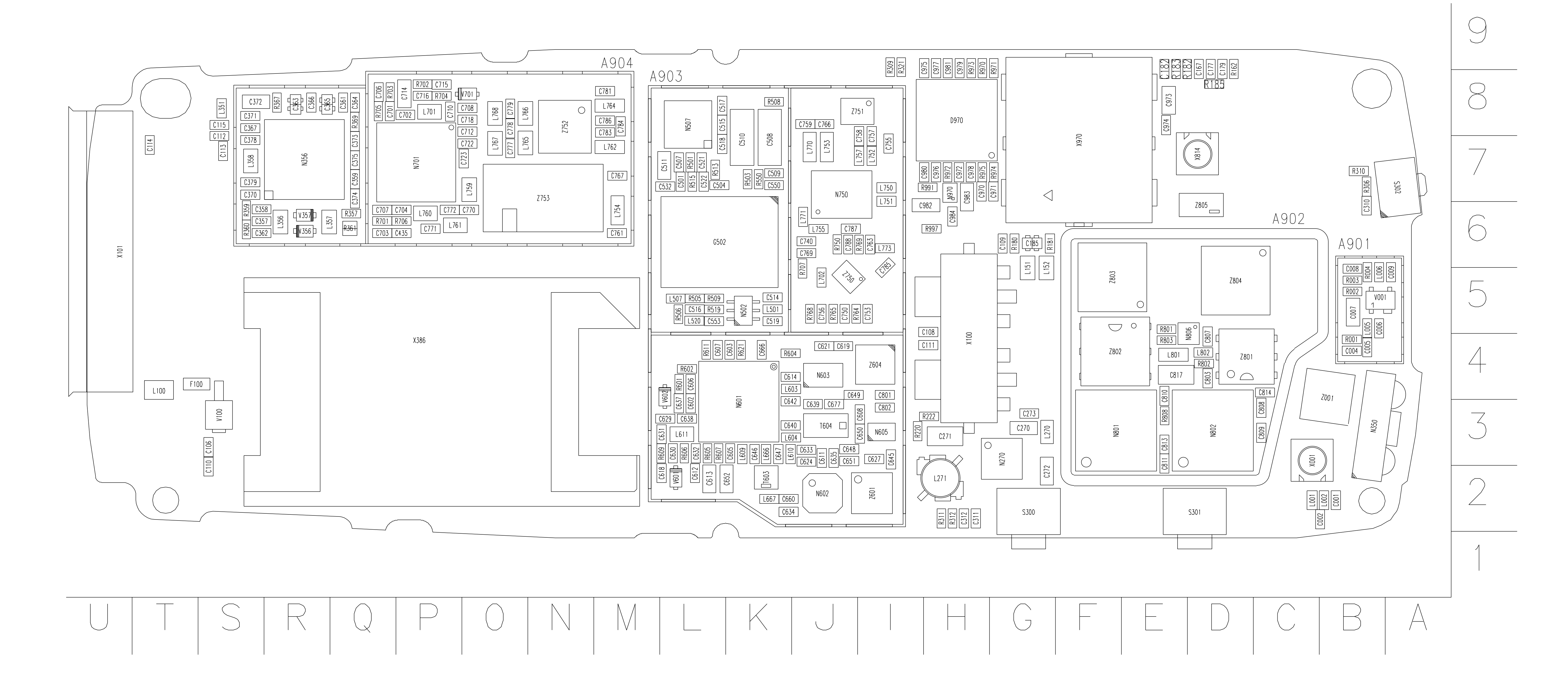
BB-RF Interface



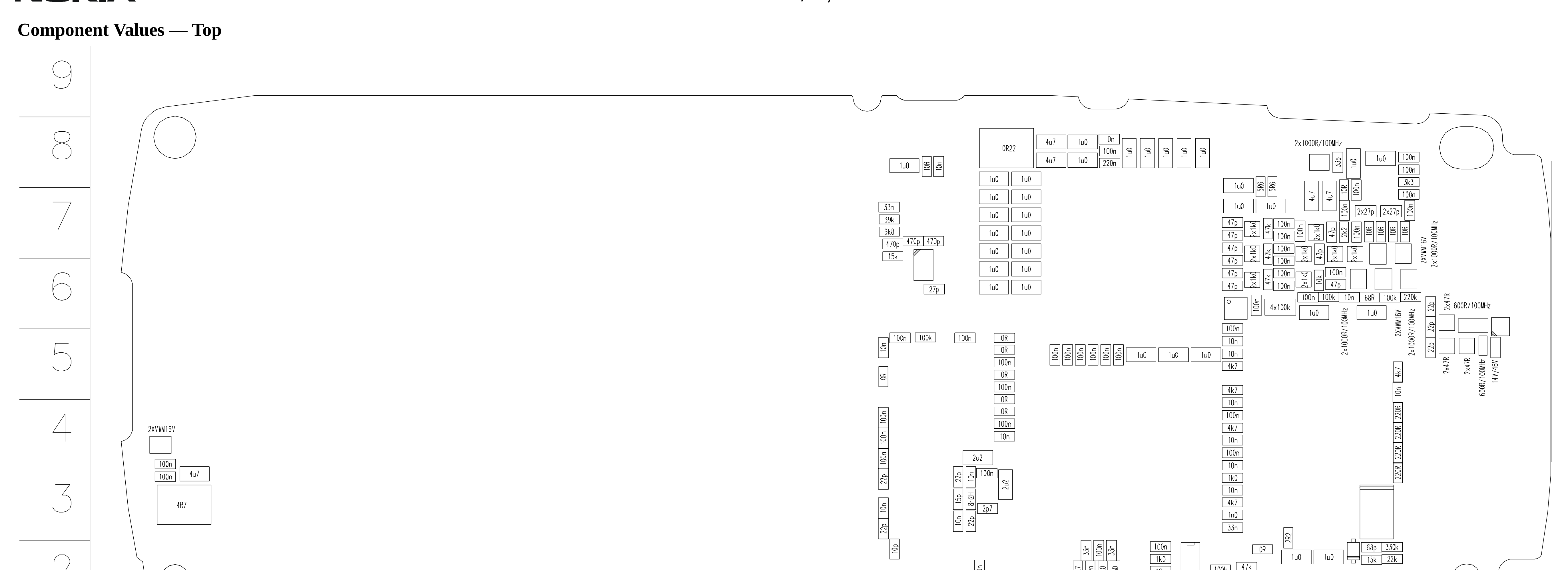
Component Layout — Top View



Component Layout — Bottom View



Component Values — Top



Component Values — Bottom

